

Abstract of the Disclosure

A row redundancy circuit comprises a fuse box group array, a redundant row predecoder and a redundant sub-row decoder. The fuse box group array comprises a plurality of fuse box groups including at least two or more fuse boxes to detect row addresses. The redundant row predecoder performs a logic operation on an output signal from the fuse box groups to selectively activate a redundant main wordline corresponding to a plurality of redundant sub-wordlines. The redundant sub-row decoder performs a logic operation on output signals from the fuse box groups, which are classified into group signals corresponding to the number of fuse boxes in each fuse box group, to output a boosting signal for selectively activating the plurality of sub-wordlines corresponding to the each redundant main wordline. In the row redundancy circuit, the current consumption due to generation of unnecessary boosting signals can be minimized because the boosting signal is prevented from being disabled and then enabled in every precharge mode.